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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **HORIGUCHI, Naoto et al.**

Group Art Unit: 2811

Serial No.: **09/726,386**

Examiner: **Tran, Thien F**

Filed: **December 1, 2000**

P.T.O. Confirmation No.: 1274

For: **SEMICONDUCTOR MEMORY WITH FLOATING GATE TYPE FET**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: August 29, 2003

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated April 30, 2003 for one month from July 30, 2003 to August 30, 2003.

Attached please find a check in the amount of \$110.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

**ARMSTRONG, WESTERMAN & HATTORI, LLP**

Thomas E. Brown  
Attorney for Applicant  
Reg. No. 44,450

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Atty. Docket No. **001497**  
Suite 1000  
1725 K Street, N.W.  
Washington, D.C. 20006  
(202) 659-2930



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PATENT TRADEMARK OFFICE